

## 1 Introduction

This tutorial shows how to develop C programs that use device driver functions for the I/O devices in a Nios® II hardware system. The device driver functions used in the tutorial are provided as part of the Intel® FPGA University Program IP cores, which are available from the University Program section of Intel's website. These functions are implemented using Intel's Hardware Abstraction Layer (HAL). In addition to providing support for device drivers, the HAL simplifies many programming tasks, such as the development of programs that use interrupts. A detailed description of the features provided by the HAL can be found in the *Nios II Software Developer's Handbook*, which is available from Intel.

The Nios II software programs shown in the tutorial are implemented by using the Intel FPGA Monitor Program development environment. This tutorial includes screen captures obtained using version 18.1 of the Intel FPGA Monitor Program; if other versions of the software are used, some of the images may be slightly different. The device driver functions used in the example programs in the tutorial are from version 18.1 of the University Program IP Cores.

We assume that the reader has access to the Intel DE2-115 Development and Education board, or the DE1-SoC board. If other boards are used, then the design examples for the tutorial may be not usable, as they require the presence of specific I/O devices.

### Contents:

- Examining the HAL device driver functions that are provided for University Program IP cores
- Writing C programs that use HAL device driver functions
- Compiling HAL code with the Monitor Program
- Running and debugging HAL code using the Monitor Program
- Finding HAL device driver source code
- Using Nios II interrupts in HAL code

## 2 Writing Programs that use HAL Device Drivers

For this tutorial, we assume that the reader is already familiar with the Intel FPGA Monitor Program. It is described in the tutorial *Introduction to the Intel FPGA Monitor Program*, which is available from the FPGA University Program section of Intel's website.

To see an example of using HAL device drivers, create a new Monitor Program project for the DE2-115 board called *HAL\_tutorial*. Store the project in a directory of your choice. When creating the project, specify the hardware system to be the prebuilt *DE2-115 Computer System*, specify the program type as **Program with Device Driver Support**, check **Include a sample program with the project**, and select the sample program named *Audio*. This sample program makes use of two types of I/O devices in the DE2-115 Computer System: parallel ports, and an audio device.

The HAL device drivers for I/O devices consist of collections of functions that allow software programs to access hardware devices. To use these functions, it is first necessary to examine the documentation provided for the IP core that connects to each I/O device, to determine the names of its device driver functions, the number and types of arguments, and the specified use of the functions. The documentation for the IP cores that are included in the DE2-115 Computer System can be found on the University Program section of Intel's website under the heading **Materials > Computer Organization > IP Cores**. As an example, the documentation file for the parallel ports provides a section called *Programming with the Parallel Ports*. A small part of this section is displayed in Figure 1. A number of device driver functions are listed in the figure, including *alt\_up\_parallel\_port\_open\_dev(...)*, which is used in C code to open a parallel port device, as well as *alt\_up\_parallel\_port\_read\_data(...)* and *alt\_up\_parallel\_port\_write\_data(...)*, which can be used to read/write data from/to a parallel port. For each function, the documentation specifies the data types of arguments and return values.

## 4.2 Programming with the Parallel Ports

The Parallel Port core is packaged with C-language device drivers accessible through the [hardware abstraction layer \(HAL\)](#). These functions implement basic operations for the Parallel Port.

To use the functions, the C code must include the statement:

```
#include "altera_up_avalon_parallel_port.h"
```

### 4.2.1 alt\_up\_parallel\_port\_open\_dev

**Prototype:** alt\_up\_parallel\_port\_dev\* alt\_up\_parallel\_port\_open\_dev(const char \*name)

**Include:** <altera\_up\_avalon\_parallel\_port.h>

**Parameters:** name – the parallel port name. For example, if the parallel port name in SOPC Builder is "green\_leds", then *name* should be "/dev/green\_leds"

**Returns:** The corresponding device structure, or NULL if the device is not found.

**Description:** Open the parallel port device specified by *name*.

### 4.2.2 alt\_up\_parallel\_port\_read\_data

**Prototype:** unsigned int alt\_up\_parallel\_port\_read\_data(alt\_up\_parallel\_port\_dev \*parallel\_port)

**Include:** <altera\_up\_avalon\_parallel\_port.h>

**Parameters:** parallel\_port – struct for the parallel port device.

**Returns:** data – The data read for the parallel port.

**Description:** Read from the data register of the parallel port.

### 4.2.3 alt\_up\_parallel\_port\_write\_data

**Prototype:** void alt\_up\_parallel\_port\_write\_data(alt\_up\_parallel\_port\_dev \*parallel\_port, unsigned data)

Figure 1. A part of the documentation file for the parallel port.

The sample program *Audio* is given as a complete example of C code that uses HAL device drivers. The C code is shown in Figure 2. This code uses two parallel ports, connected to switches and LEDs on the DE2-115 board, and an audio port. The code performs the following operations:

1. When the switch *KEY*<sub>1</sub> is pressed on the DE2-115 board, audio input from the microphone jack is recorded for about 10 seconds. *LEDG*<sub>0</sub> is illuminated while recording.
2. When the switch *KEY*<sub>2</sub> is pressed, the recorded audio is played back on the line-out jack. *LEDG*<sub>1</sub> is illuminated during playback.

As shown in Figure 2(a), the C code first includes the necessary header files for the parallel port and audio devices. In lines 7–9 a pointer is declared for each of the three I/O devices to be used in the code. The pointers have a special type, which is the type of return value for the device driver function that opens the device—for example, in Figure 1 the data type of the function *alt\_up\_parallel\_port\_open\_dev(...)* is *alt\_up\_parallel\_port\_dev*. Lines 10–12 of the code open the two parallel ports and the audio device needed in the program. As shown, each I/O device is referenced using a unique name. The names of the two parallel port devices are *Pushbuttons* and *Green\_LEDs*, and the audio device is named *Audio\_Subsystem\_Audio*. These names are prefixed with the string */dev/*, and then passed

to the device driver function. The unique name of each device is assigned by the designer when the Nios II hardware system is created by using Intel's Platform Designer tool. Figure 3 shows part of the *Systems Contents* that can be displayed in the Platform Designer tool for the DE2-115 Computer System, with the module names (without the */dev/* prefix) displayed in the column labeled *Module Name*. Figure 4 shows the *Systems Contents* that can be displayed in the Platform Designer tool for the Audio Subsystem, with the module names (without the */dev/Audio\_Subsystem\_* prefix) displayed in the column labeled *Module Name*.

```

1  #include "altera_up_avalon_parallel_port.h"
2  #include "altera_up_avalon_audio.h"
   /* globals */
3  #define BUF_SIZE 500000          // about 10 seconds of buffer (@ 48K samples/sec)
4  #define BUF_THRESHOLD 96        // 75% of 128 word buffer
   /* function prototypes */
5  void check_KEYS( int *, int *, int *, alt_up_parallel_port_dev *, alt_up_audio_dev * );
   /******
   * This program demonstrates use of the media ports in the DE2-115 Computer System
   *
   * It performs the following:
   *   1. records audio for about 10 seconds when KEY[1] is pressed. LEDG[0] is
   *      lit while recording
   *   2. plays the recorded audio when KEY[2] is pressed. LEDG[1] is lit while
   *      playing
   *
   *****/
6  int main(void)
   {
   /* declare variables to point to devices that are opened */
7     alt_up_parallel_port_dev *KEY_dev;
8     alt_up_parallel_port_dev *green_LEDs_dev;
9     alt_up_audio_dev *audio_dev;
   // open the pushbutton KEY parallel port
10    KEY_dev = alt_up_parallel_port_open_dev ("/dev/Pushbuttons");
   // open the green LEDs parallel port
11    green_LEDs_dev = alt_up_parallel_port_open_dev ("/dev/Green_LEDs");
   // open the audio port
12    audio_dev = alt_up_audio_open_dev ("/dev/Audio_Subsystem_Audio");
   /* used for audio record/playback */
13    int record = 0, play = 0, buffer_index = 0;
14    unsigned int l_buf[BUF_SIZE];
15    unsigned int r_buf[BUF_SIZE];
16    int num_read; int num_written;

```

Figure 2. An example of C code that uses HAL device drivers (Part a).

```
/* read and echo audio data */
17  record = 0;
18  play = 0;
19  while(1)
    {
20      check_KEYS (&record, &play, &buffer_index, KEY_dev, audio_dev);
21      if (record)
        {
22          alt_up_parallel_port_write_data (green_LEDs_dev, 0x1); // set LEDG[0] on
                // record data until the buffer is full
23          if (buffer_index < BUF_SIZE)
            {
24              num_read = alt_up_audio_record_r (audio_dev, &(r_buf[buffer_index]),
                    BUF_SIZE - buffer_index);
                /* assume we can read same # words from the left and right */
25              (void) alt_up_audio_record_l (audio_dev, &(l_buf[buffer_index]), num_read);
26              buffer_index += num_read;
27              if (buffer_index == BUF_SIZE)
                {
                // done recording
28                  record = 0;
29                  alt_up_parallel_port_write_data (green_LEDs_dev, 0x0); // turn off LEDG
                }
            }
        }
    }
```

Figure 2. An example of C code that uses HAL device drivers (Part b).

The rest of the code in Figure 2 performs the recording and playback of audio, and controls the pushbutton and green lights parallel ports. Device driver functions are called in the main program in lines 22, 24–25, 29, 31, 33–34, and 38, and in the function *check\_KEYS*, which examines the values of the pushbutton switches, in lines 41–42, 45, and 49. The operation of each of these functions is described in the documentation file for the corresponding IP core, as discussed previously for Figure 1.

```

30     else if (play)
31     {
32         alt_up_parallel_port_write_data(green_LEDs_dev, 0x2); // set LEDG[1] on
33         // output data until the buffer is empty
34         if (buffer_index < BUF_SIZE)
35         {
36             num_written = alt_up_audio_play_r(audio_dev, &(r_buf[buffer_index]),
37             BUF_SIZE - buffer_index);
38             /* assume that we can write the same # words to the left and right */
39             (void) alt_up_audio_play_l(audio_dev, &(l_buf[buffer_index]), num_written);
40             buffer_index += num_written;
41
42             if (buffer_index == BUF_SIZE) // done playback
43             {
44                 play = 0;
45                 alt_up_parallel_port_write_data(green_LEDs_dev, 0x0); // turn off LEDG
46             }
47         }
48     }
49 }
50
51 /*****
52  * Subroutine to read KEYs
53  *****/
54 void check_KEYS(int *KEY1, int *KEY2, int *counter, alt_up_parallel_port_dev *KEY_dev,
55                alt_up_audio_dev *audio_dev)
56 {
57     int KEY_value;
58     KEY_value = alt_up_parallel_port_read_data (KEY_dev); // read the pushbutton KEY values
59     while (alt_up_parallel_port_read_data (KEY_dev)); // wait for pushbutton KEY release
60     if (KEY_value == 0x2) // check KEY1
61     {
62         *counter = 0; // reset counter to start recording
63         alt_up_audio_reset_audio_core (audio_dev); // reset audio port
64         *KEY1 = 1;
65     }
66     else if (KEY_value == 0x4) // check KEY2
67     {
68         *counter = 0; // reset counter to start playback
69         alt_up_audio_reset_audio_core (audio_dev); // reset audio port
70         *KEY2 = 1;
71     }
72 }

```

Figure 2. An example of C code that uses HAL device drivers (Part c).

Use	C...	Name	Description	Ex...	Clock	Base	End	IRQ
<input checked="" type="checkbox"/>		<b>System_PLL</b>	System and SDRAM Clocks for DE-seri...		<i>exported</i>			
<input checked="" type="checkbox"/>		<b>Video_PLL</b>	Video Clocks for DE-series Boards		<i>exported</i>			
<input checked="" type="checkbox"/>		<b>Nios2</b>	Nios II Processor		<b>System_PL...</b>	0x0a00_0000	0x0a00_07ff	
<input checked="" type="checkbox"/>		<b>Nios2_Floating_Point</b>	Floating Point Hardware			Opcode 252	Opcode 255	
<input checked="" type="checkbox"/>		<b>Nios2_2nd_Core</b>	Nios II Processor		<b>System_PL...</b>	0x0a00_0000	0x0a00_07ff	
<input checked="" type="checkbox"/>		<b>Nios2_2nd_Core_Flo...</b>	Floating Point Hardware			Opcode 252	Opcode 255	
<input checked="" type="checkbox"/>		<b>JTAG_to_FPGA_B...</b>	JTAG to Avalon Master Bridge		<b>System_PL...</b>			
<input checked="" type="checkbox"/>		<b>SDRAM</b>	SDRAM Controller Intel FPGA IP		<b>System_PL...</b>	0x0000_0000	0x07ff_ffff	
<input checked="" type="checkbox"/>		<b>SRAM</b>	SRAM Controller		<b>System_PL...</b>	0x0800_0000	0x081f_ffff	
<input checked="" type="checkbox"/>		<b>SD_Card</b>	SD Card Interface		<b>System_PL...</b>	0x0b00_0000	0x0b00_03ff	
<input checked="" type="checkbox"/>		<b>Flash</b>	Intel FPGA University Program Flash M...		<b>System_PL...</b>		multiple	
<input checked="" type="checkbox"/>		<b>Red_LEDs</b>	PIO (Parallel I/O) Intel FPGA IP		<b>System_PL...</b>	0xff20_0000	0xff20_000f	
<input checked="" type="checkbox"/>		<b>Green_LEDs</b>	PIO (Parallel I/O) Intel FPGA IP		<b>System_PL...</b>	0xff20_0010	0xff20_001f	
<input checked="" type="checkbox"/>		<b>HEX3_HEX0</b>	PIO (Parallel I/O) Intel FPGA IP		<b>System_PL...</b>	0xff20_0020	0xff20_002f	
<input checked="" type="checkbox"/>		<b>HEX7_HEX4</b>	PIO (Parallel I/O) Intel FPGA IP		<b>System_PL...</b>	0xff20_0030	0xff20_003f	
<input checked="" type="checkbox"/>		<b>Slider_Switches</b>	PIO (Parallel I/O) Intel FPGA IP		<b>System_PL...</b>	0xff20_0040	0xff20_004f	
<input checked="" type="checkbox"/>		<b>Pushbuttons</b>	PIO (Parallel I/O) Intel FPGA IP		<b>System_PL...</b>	0xff20_0050	0xff20_005f	
<input checked="" type="checkbox"/>		<b>Expansion_JP5</b>	PIO (Parallel I/O) Intel FPGA IP		<b>System_PL...</b>	0xff20_0060	0xff20_006f	
<input checked="" type="checkbox"/>		<b>PS2_Port</b>	PS/2 Controller		<b>System_PL...</b>	0xff20_0100	0xff20_010f	
<input checked="" type="checkbox"/>		<b>PS2_Port_Dual</b>	PS/2 Controller		<b>System_PL...</b>	0xff20_0108	0xff20_010f	
<input checked="" type="checkbox"/>		<b>USB</b>	USB Controller		<b>System_PL...</b>	0xff20_0110	0xff20_011f	
<input checked="" type="checkbox"/>		<b>JTAG_UART</b>	JTAG UART Intel FPGA IP		<b>System_PL...</b>	0xff20_1000	0xff20_1007	
<input checked="" type="checkbox"/>		<b>JTAG_UART_2nd_Co...</b>	JTAG UART Intel FPGA IP		<b>System_PL...</b>	0xff20_1000	0xff20_1007	
<input checked="" type="checkbox"/>		<b>Serial_Port</b>	RS232 UART		<b>System_PL...</b>	0xff20_1010	0xff20_1017	
<input checked="" type="checkbox"/>		<b>IrDA</b>	IrDA UART		<b>System_PL...</b>	0xff20_1020	0xff20_1027	
<input checked="" type="checkbox"/>		<b>Interval_Timer</b>	Interval Timer Intel FPGA IP		<b>System_PL...</b>	0xff20_2000	0xff20_201f	
<input checked="" type="checkbox"/>		<b>Interval_Timer_2</b>	Interval Timer Intel FPGA IP		<b>System_PL...</b>	0xff20_2020	0xff20_203f	
<input checked="" type="checkbox"/>		<b>Interval_Timer_2nd...</b>	Interval Timer Intel FPGA IP		<b>System_PL...</b>	0xff20_2000	0xff20_201f	
<input checked="" type="checkbox"/>		<b>Interval_Timer_2nd...</b>	Interval Timer Intel FPGA IP		<b>System_PL...</b>	0xff20_2020	0xff20_203f	
<input checked="" type="checkbox"/>		<b>SysID</b>	System ID Peripheral Intel FPGA IP		<b>System_PL...</b>	0xff20_2040	0xff20_2047	
<input checked="" type="checkbox"/>		<b>AV_Config</b>	Audio and Video Config		<b>System_PL...</b>	0xff20_3000	0xff20_300f	
<input checked="" type="checkbox"/>		<b>VGA_Subsystem</b>	VGA_Subsystem		<i>multiple</i>	# multiple	multiple	
<input checked="" type="checkbox"/>		<b>Audio_Subsystem</b>	Audio_Subsystem		<i>multiple</i>	0xff20_3040	0xff20_304f	
<input checked="" type="checkbox"/>		<b>Char_LCD_16x2</b>	16x2 Character Display		<b>System_PL...</b>	0xff20_3050	0xff20_3051	
<input checked="" type="checkbox"/>		<b>Video_In_Subsys...</b>	Video_In_Subsystem		<b>System_PL...</b>	multiple	multiple	

Figure 3. Module names for the DE2-115 Computer System shown in Platform Designer.

Use	C...	Name	Description	Ex...	Clock	Base	End	IRQ
<input checked="" type="checkbox"/>		<b>Sys_Clk</b>	Clock Source		<i>exported</i>			
<input checked="" type="checkbox"/>		<b>Audio_PLL</b>	Audio Clock for DE-series Boards		<i>exported</i>			
<input checked="" type="checkbox"/>		<b>Audio</b>	Audio		<b>Sys_Clk</b>	#		

Figure 4. Module names for the DE2-115 Audio Subsystem shown in Platform Designer.

### 3 Compiling Programs that use Device Drivers

The *HAL\_tutorial* project can be compiled in the normal way by using the Monitor Program commands **Actions > Compile**, or **Actions > Compile & Load**. The first time this is done, the Monitor Program compiles not only the file *audio\_HAL.c*, but also a number of C library functions that are a part of the HAL system, and all of the device driver functions that are provided for every device in the Nios II hardware system. Although this process is somewhat time consuming, it is only done once, and subsequent compilations only compile the source file *audio\_HAL.c*. If it is necessary to recompile all of the HAL functions at a later time, this can be accomplished by using the command **Actions > Regenerate Device Drivers (BSP)**. This action might be necessary, for example, if a new version of IP cores is installed at a later time.

### 3.1 Running the Program

Programs that use HAL device drivers can be run and debugged in the Monitor Program in the same way as other C or assembly language programs. Figure 5 shows an example of a breakpoint set in the code of Figure 2. The figure shows the value read from the pushbutton parallel port by the device driver function *alt\_up\_parallel\_port\_read\_data(...)*. This particular device driver is executed without using a *call* assembly language instruction, which means that it is implemented as a macro, rather than a subroutine. The value returned by the function is shown in the Nios II register *r3*. The value is 0x00000002, which means that *KEY<sub>1</sub>* on the DE2-115 board was pressed when the function was called. Other device drivers are implemented as subroutines, such as *alt\_up\_audio\_record\_r(...)*, as indicated in Figure 6. In the figure, a breakpoint has been set at this function call. The subroutine can be executed in a debugging session by single-stepping into the device driver code if desired.

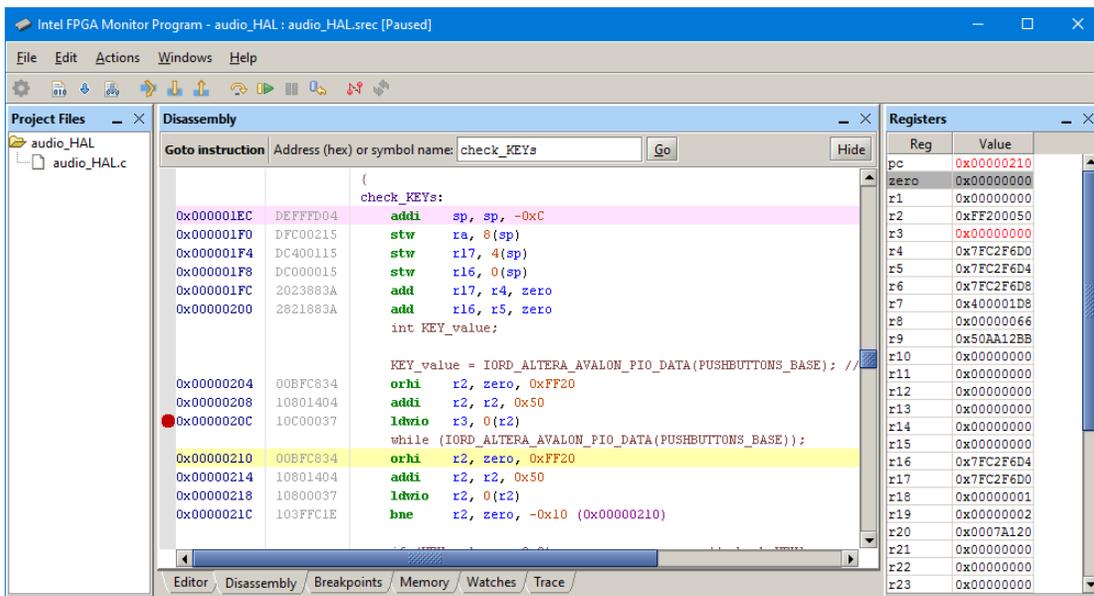


Figure 5. An example of a HAL device driver function that is implemented as a macro.

## 4 Examining the HAL Device Driver Source Code

It is possible to examine the source code of the device driver functions used in the HAL. These functions are installed in the same filesystem directory as the Quartus® Prime software, as illustrated on the left side of Figure 7. In this figure, `QUARTUS_ROOTDIR`<sup>1</sup> represents the installation directory of the Quartus Prime software. The device driver code is stored in two directories: the *inc* directory contains device driver subroutine prototypes and device driver macros, and the *src* directory contains subroutine definitions. During the process of compiling a project in the Monitor Program, the *inc* and *src* directories for all IP cores are copied into the project's directory. As shown on the right side of Figure 7, the directories are copied into a folder called *drivers*, which is a subfolder of the folder called *BSP* (the acronym stands for *Board Support Package*). The figure shows the directory structure of the parallel port

<sup>1</sup>In Windows operating systems, the environment variable `QUARTUS_ROOTDIR` points to the folder where Quartus Prime software is installed.

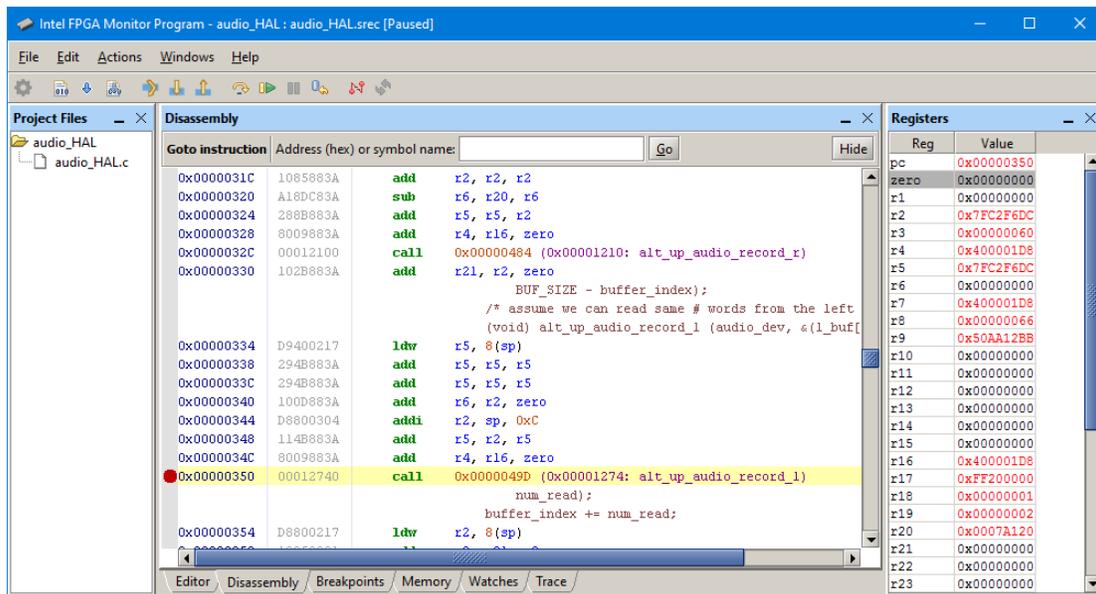


Figure 6. An example of a HAL device driver function implemented as a subroutine.

and audio IP cores as an example, but the source files for all of the IP cores in the hardware system are copied in this way.

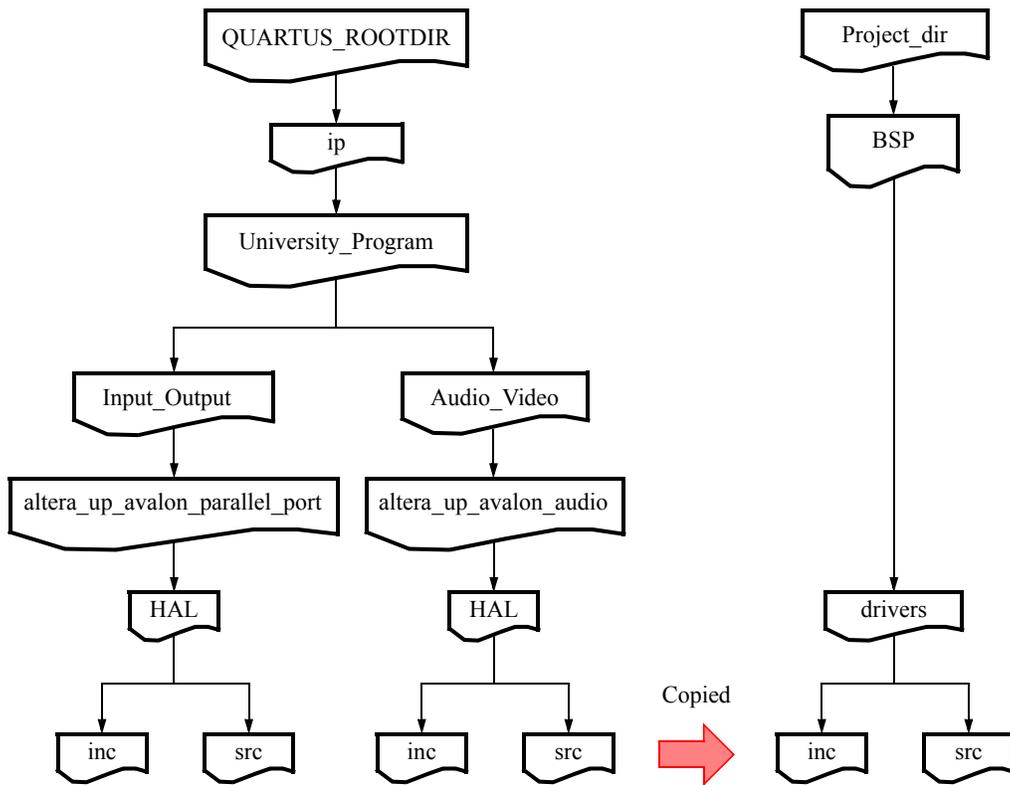


Figure 7. Finding the HAL device driver source code files.

## 5 Using Nios® II Interrupts in the HAL

The HAL provides a simple interface for using Nios II interrupts in C programs. To use interrupts programs must specify the statement `#include "sys/alt_irq.h"`. This include file provides a function named `alt_irq_register (...)`, which is used to specify the hardware interrupt levels and associated interrupt service routines for any Nios II interrupts that are being used. To see an example that uses interrupts, create a new Monitor Program project for the DE2-115 board called `HAL_tutorial_int`. Store the project in a directory of your choice. When creating the project, specify the hardware system to be the *DE2-115 Computer System*, specify the program type as Program with Device Driver Support, check Include a sample program with the project, and select the sample program provided in the Monitor Program named `interrupts_example`. This sample program has the exact same functionality as the code in Figure 2, except that the pushbutton `KEY` parallel port and audio device are handled using interrupts. There are three source files in this program: `interrupts_HAL.c` contains the main program, and the interrupt service routines are found in the files `pushbutton_ISR.c` and `audio_ISR.c`.

Figure 8 shows the C code for the main program. It first includes the file `globals.h`, which, as illustrated in Figure 9, includes the parallel port and audio HAL header files `altera_up_avalon_parallel_port.h` and `altera_up_avalon_audio.h`. The `globals.h` file also includes `sys/alt_stdio.h` and `alt_irq.h`. As mentioned above, `alt_irq.h` is needed to use interrupts with the HAL. The file `alt_stdio.h` defines some simplified versions of the functions in the standard C library `stdio.h`. The purpose of `alt_stdio.h` is to conserve memory space in the hardware system by providing functions that have limited capability but also produce less machine code when compiled. In this example, we will use a function called `alt_printf`, which is a simplified version of `printf`. The use of such functions is not necessary for the example program, and is provided only for illustrative purposes. The `alt_stdio.h` library, and other C libraries provided by Intel, is described in the *Nios II Software Developer's Handbook*, available from Intel's website.

The last few lines of code in Figure 9 declare a C structure named `alt_up_dev`. As indicated in the code, this structure is used to hold a pointer to the I/O devices for the two parallel ports and audio port. We will explain the purpose of this structure shortly. Referring back to the main program in Figure 8, line 2 defines an instance, named `up_dev`, of the `alt_up_dev` structure. Line 3 defines two variables used with the audio device. These variables have to be declared using the keyword `volatile`, because their values are written by interrupt service routines. If this keyword is not used, then the C compiler may choose to save the value of the variable in a Nios II general-purpose register and to retrieve the value of this variable, when needed in a program, from this register, rather than from memory. In this case, changes to the variable's value that are written into memory by an interrupt service routine would not be seen in the main program. Using the `volatile` keyword prohibits the C compiler from saving the value of the variable in a CPU register, and causes the Nios II processor to access the variable using `load I/O` and `store I/O` instructions<sup>2</sup>.

---

<sup>2</sup> Even if a version of the Nios II processor that has a *data cache* is being used, the load I/O instruction causes the processor to bypass this cache and access the associated variable at its address in memory.



```

    /* write to the pushbutton interrupt mask register, and set 3 mask bits to 1
       * (we can't use pushbutton 0; it is the Nios II reset button) */
17  alt_up_parallel_port_set_interrupt_mask (KEY_dev, 0xE);
    // open the green LEDs parallel port
18  green_LEDs_dev = alt_up_parallel_port_open_dev ("/dev/Green_LEDs");
19  if (green_LEDs_dev == NULL)
    {
20      alt_printf ("Error: could not open green LEDs device\n");
21      return -1;
    }
22  else
    {
23      alt_printf ("Opened green LEDs device\n");
24      up_dev.green_LEDs_dev = green_LEDs_dev; // store for use by ISRs
    }
    // open the audio port
25  audio_dev = alt_up_audio_open_dev ("/dev/Audio_Subsystem_Audio");
26  if (audio_dev == NULL)
    {
27      alt_printf ("Error: could not open audio device\n");
28      return -1;
    }
29  else
    {
30      alt_printf ("Opened audio device\n");
31      up_dev.audio_dev = audio_dev; // store for use by ISRs
    }
    /* use the HAL facility for registering interrupt service routines. */
    /* Note: we are passing a pointer to up_dev to each ISR (using the HAL context argument) as
       * a way of giving the ISR a pointer to every open device. This is useful because some of the
       * ISRs need to access more than just one device (e.g. the pushbutton ISR accesses both
       * the pushbutton device and the audio device) */
32  alt_irq_register (1, (void *) &up_dev, (void *) pushbutton_ISR);
33  alt_irq_register (6, (void *) &up_dev, (void *) audio_ISR);
    /* the main program can now exit; further program actions are handled by interrupts */
}

```

Figure 8. An example of C code that uses interrupts (Part b).

Lines 7–31 of Figure 8 open the three I/O devices needed in the program, and use *alt\_printf* to display an appropriate error if a device cannot be properly opened. Although this check is not strictly needed in our example, it is a good practice to check the return value of functions for any errors that may occur. The pushbutton KEYs parallel port is configured to generate hardware interrupts by using the HAL function shown in line 17 of the code. Lines 16, 24, and 31 store the pointer to each opened I/O device in the *up\_dev* structure. This structure is used in lines 32 and 33, which call the *alt\_irq\_register* function. The first argument to this function is the level of the hardware interrupt

being used. In the DE2-115 Computer System hardware system, the pushbutton KEYs parallel port is assigned interrupt level 1 and the audio port has interrupt level 6. The second argument of *alt\_irq\_register* is a pointer of type *void \** called the *context* pointer. This pointer is simply passed on to the associated interrupt service routine (ISR) when the interrupt occurs; it can point to any type of object and can be used for any purpose needed in the ISR. In this case, we pass a pointer to the *up\_dev* structure to the ISR for both the pushbutton parallel port and audio port. Since *up\_dev* holds a pointer to all of the open I/O devices, it allows an ISR to execute device driver functions for any of the devices. The last argument to *alt\_irq\_register* is a pointer to the associated ISR. Each ISR has to have two arguments, as illustrated in lines 4 and 5 of Figure 8. The first of these arguments is used for the context pointer, and the second argument gives the interrupt level.

The interrupt service routine for the pushbutton parallel port is given in Figure 10. As shown in the code, it uses the *up\_dev* context pointer to access both the pushbutton parallel port and audio port. Line 8 uses a device driver function to determine which pushbutton KEY caused the interrupt, and line 9 clears this interrupt. If KEY<sub>1</sub> is pressed, then read interrupts are enabled for the audio device to begin recording, and if KEY<sub>2</sub> is pressed, then write interrupts are enabled for the audio device to perform playback.

Figure 11 shows the ISR for the audio device. It is very similar to the code in Figure 2, except that line 8 checks for audio read interrupts, and line 16 disables audio read interrupts when the recording buffer is full. Also, line 17 checks for audio write interrupts, and line 25 disables these interrupts when the playback buffer is empty.

```
/* include HAL device driver functions for the parallel port and audio device */
#include "altera_up_avalon_parallel_port.h"
#include "altera_up_avalon_audio.h"

#include "sys/alt_stdio.h"
#include "sys/alt_irq.h"

/* This structure holds a pointer to each open I/O device */
struct alt_up_dev {
    alt_up_parallel_port_dev *KEY_dev;
    alt_up_parallel_port_dev *green_LEDs_dev;
    alt_up_audio_dev *audio_dev;
};
```

Figure 9. Include files and a structure to hold pointers.

```

1  #include "globals.h"
   /* indices for audio record and playback; we reset them when pushbuttons are pressed */
2  extern volatile int buf_index_record;
3  extern volatile int buf_index_play;
   /*******
   * Pushbutton - Interrupt Service Routine
   *
   * This ISR checks which KEY has been pressed. If KEY1, then it enables audio-in
   * interrupts (recording). If KEY2, it enables audio-out interrupts (playback).
   *****/
4  void pushbutton_ISR(struct alt_up_dev *up_dev, unsigned int id)
   {
5     alt_up_audio_dev *audio_dev;
6     audio_dev = up_dev->audio_dev;
7     int KEY_value;
   /* read the pushbutton interrupt register */
8     KEY_value = alt_up_parallel_port_read_edge_capture (up_dev->KEY_dev);
9     alt_up_parallel_port_clear_edge_capture (up_dev->KEY_dev); // clear the interrupt
10    if (KEY_value == 0x2)           // check KEY1
        {
11        // reset the buffer index for recording
            buf_index_record = 0;
12        // clear audio FIFOs
            alt_up_audio_reset_audio_core (audio_dev);
            // enable audio-in interrupts
13        alt_up_audio_enable_read_interrupt (audio_dev);
        }
14    else if (KEY_value == 0x4)       // check KEY2
        {
15        // reset counter to start playback
            buf_index_play = 0;
16        // clear audio FIFOs
            alt_up_audio_reset_audio_core (audio_dev);
            // enable audio-out interrupts
17        alt_up_audio_enable_write_interrupt (audio_dev);
        }
18    return;
   }

```

Figure 10. The pushbutton *KEY* interrupt service routine.

```

1  #include "globals.h"
2  #define BUF_SIZE 500000          // about 10 seconds of audio buffer (@ 48K samples/sec)
   /* globals used for audio record/playback */
3  extern volatile int buf_index_record, buf_index_play;
4  unsigned int l_buf[BUF_SIZE];   // audio buffer
5  unsigned int r_buf[BUF_SIZE];   // audio buffer
   /**
   * Audio - Interrupt Service Routine
   * This interrupt service routine records or plays back audio, depending on which type
   * interrupt (read or write) is pending in the audio device.
   */
6  void audio_ISR(struct alt_up_dev *up_dev, unsigned int id)
   {
7     int num_read, num_written;
8     if (alt_up_audio_read_interrupt_pending(up_dev->audio_dev))          // check for read interrupt
9     {
10        alt_up_parallel_port_write_data (up_dev->green_LEDs_dev, 0x1);    // set LEDG[0] on
11        // store data until the buffer is full
12        if (buf_index_record < BUF_SIZE)
13        {
14            num_read = alt_up_audio_record_r (up_dev->audio_dev, &(r_buf[buf_index_record]),
15            BUF_SIZE - buf_index_record);
16            /* assume we can read same # words from the left and right */
17            (void) alt_up_audio_record_l (up_dev->audio_dev, &(l_buf[buf_index_record]), num_read);
18            buf_index_record += num_read;
19            if (buf_index_record == BUF_SIZE)                                // done recording
20            {
21                alt_up_parallel_port_write_data (up_dev->green_LEDs_dev, 0); // turn off LEDG
22                alt_up_audio_disable_read_interrupt(up_dev->audio_dev);
23            }
24        }
25    }
26 }

```

Figure 11. The audio device interrupt service routine (Part a).

```

17  if (alt_up_audio_write_interrupt_pending(up_dev->audio_dev))           // check for write interrupt
    {
18      alt_up_parallel_port_write_data (up_dev->green_LEDs_dev, 0x2);     // set LEDG[1] on
        // output data until the buffer is empty
19      if (buf_index_play < BUF_SIZE)
        {
20          num_written = alt_up_audio_play_r (up_dev->audio_dev, &(r_buf[buf_index_play]),
            BUF_SIZE - buf_index_play);
            /* assume that we can write the same # words to the left and right */
21          (void) alt_up_audio_play_l (up_dev->audio_dev, &(l_buf[buf_index_play]), num_written);
22          buf_index_play += num_written;
23          if (buf_index_play == BUF_SIZE)                                 // done playback
            {
24              alt_up_parallel_port_write_data (up_dev->green_LEDs_dev, 0); // turn off LEDG
25              alt_up_audio_disable_write_interrupt(up_dev->audio_dev);
            }
        }
    }
26  return;
}

```

Figure 11. The audio device interrupt service routine (Part *b*).

## 6 Final Remarks

In this tutorial we have introduced the use of HAL device drivers with C programs. We have shown that C code using device driver functions can be developed by following the steps below:

1. Obtain a Nios II hardware system for which the IP cores in the system include HAL device drivers. In this tutorial we used the DE2-115 Computer System hardware system. It uses the University Program IP cores that are available from the University Program section of Intel's website.
2. Examine the documentation provided for the IP cores in the hardware system. This documentation includes a section that describes the available HAL device driver functions for the IP core.
3. Create an Intel FPGA Monitor Program project for the hardware system, with the program type set to Program with Device Driver Support.
4. Based on the functionality needed, write a program that calls the necessary HAL device driver functions. The include files that define the device driver functions must be included in the program. If interrupts are needed, then an interrupt service for each interrupt can be registered by using the *alt\_irq\_register* library function. Other mechanisms for dealing with interrupts are also available, and are described in the document *Nios II Software Developer's Handbook*, which is provided by Intel.
5. Compile the program in the normal way by using the provided commands in the Monitor Program. Debug the program by setting breakpoints, single-stepping, examining memory, and so on, using the provided features of the Monitor Program.

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